



```

ISim 0.61xd (signature 0x1cce1bb2)
This is a Full version of ISim.
Time resolution is 1 ps
Simulator is doing circuit initialization process.
0ns - *** Starting enc8b10bx12 Testbench Simulation ***
Finished circuit initialization process.
ISim>
# run 21.00us
13632ns - *** enc8b10bx12 Testbench Simulation COMPLETE ***
Stopped at time : 13632 ns : in File "C:/Users/tgrassi/XilinxDesigns/enc8b10bx12_v8/enc8b10bx12_tb.v" Line 1229
ISim>
  
```