Core #1-7	**HF** Slot #	**HF** Name	**H0** Slot #	**HO** Name	**HE** Slot #	**HE** Name	**HB** Slot #	**HB** Name	CCM_I2C device_MEMORY_MAP_v2_2_7_2008.xls Device type	I2C Register name	I2C address	note
1	1	CM-Pulser	1	RM4-RB1	1	RM1-RB1	1	RM4-RB1	CCA#1	Pointer Register	00 0000 0	
1	1	CM-Pulser	1	RM4-RB1	1	RM1-RB1	1	RM4-RB1	CCA#1	Data Register	00 0000 1	The Core number is selected by writing to the CCM module.
1	1	CM-Pulser	1	RM4-RB1	1	RM1-RB1	1	RM4-RB1	CCA#2	Pointer Register	00 0001 0	
1	1	CM-Pulser	1	RM4-RB1	1	RM1-RB1	1	RM4-RB1	CCA#2	Data Register	00 0001 1	
1	1	CM-Pulser	1	RM4-RB1	1	RM1-RB1	1	RM4-RB1	CCA#3	Pointer Register	00 0010 0	The 2 MSB are the geographical address of the 3 FE board
1	1	CM-Pulser	1	RM4-RB1	1	RM1-RB1	1	RM4-RB1	CCA#3	Data Register	00 0010 1	group.
1	1	CM-Pulser	1	RM4-RB1	1	RM1-RB1	1	RM4-RB1	GOL#1	Pointer Register	00 0011 0	
1	1	CM-Pulser	1	RM4-RB1	1	RM1-RB1	1	RM4-RB1	GOL#1	Data Register	00 0011 1	
1	1	CM-Pulser	1	RM4-RB1	1	RM1-RB1	1	RM4-RB1	GOL#2	Pointer Register	00 0100 0	The middle 4 bits are hard wired on the FE board for each
1	1	CM-Pulser	1	RM4-RB1	1	RM1-RB1	1	RM4-RB1	GOL#2	Data Register	00 0100 1	I2C device. All FE boards are the same.
1	2	CM-Pulser	2	RM4-RB2	2	RM1-RB2	2	RM4-RB2	CCA#1	Pointer Register	01 0000 0	
1	2	CM-Pulser	2	RM4-RB2	2	RM1-RB2	2	RM4-RB2	CCA#1	Data Register	01 0000 1	
1	2	CM-Pulser	2	RM4-RB2	2	RM1-RB2	2	RM4-RB2	CCA#2	Pointer Register	01 0001 0	The LSB is a value that is sent to the I2C device to select the
1	2	CM-Pulser	2	RM4-RB2	2	RM1-RB2	2	RM4-RB2	CCA#2	Data Register	01 0001 1	pointer or data register within the I2C device.
1	2	CM-Pulser	2	RM4-RB2	2	RM1-RB2	2	RM4-RB2	CCA#3	Pointer Register	01 0010 0	
1	2	CM-Pulser	2	RM4-RB2	2	RM1-RB2	2	RM4-RB2	CCA#3	Data Register	01 0010 1	
1	2	CM-Pulser	2	RM4-RB2	2	RM1-RB2	2	RM4-RB2	GOL#1	Pointer Register	01 0011 0	
1	2	CM-Pulser	2	RM4-RB2	2	RM1-RB2	2	RM4-RB2	GOL#1	Data Register	01 0011 1	
1	2	CM-Pulser	2	RM4-RB2	2	RM1-RB2	2	RM4-RB2	GOL#2	Pointer Register	01 0100 0	
1	2	CM-Pulser	2	RM4-RB2	2	RM1-RB2	2	RM4-RB2	GOL#2	Data Register	01 0100 1	
1	3	CM-Pulser	3	RM4-RB3	3	RM1-RB3	3	RM4-RB3	CCA#1	Pointer Register	10 0000 0	
1	3	CM-Pulser	3	RM4-RB3	3	RM1-RB3	3	RM4-RB3	CCA#1	Data Register	10 0000 1	
1	3	CM-Pulser	3	RM4-RB3	3	RM1-RB3	3	RM4-RB3	CCA#2	Pointer Register	10 0001 0	
1	3	CM-Pulser	3	RM4-RB3	3	RM1-RB3	3	RM4-RB3	CCA#2	Data Register	10 0001 1	
1	3	CM-Pulser	3	RM4-RB3	3	RM1-RB3	3	RM4-RB3	CCA#3	Pointer Register	10 0010 0	
1	3	CM-Pulser	3	RM4-RB3	3	RM1-RB3	3	RM4-RB3	CCA#3	Data Register	10 0010 1	
1	3	CM-Pulser	3	RM4-RB3	3	RM1-RB3	3	RM4-RB3	GOL#1	Pointer Register	10 0011 0	
1	3	CM-Pulser	3	RM4-RB3	3	RM1-RB3	3	RM4-RB3	GOL#1	Data Register	10 0011 1	
1	3	CM-Pulser	3	RM4-RB3	3	RM1-RB3	3	RM4-RB3	GOL#2	Pointer Register	10 0100 0	
1	3	CM-Pulser	3	RM4-RB3	3	RM1-RB3	3	RM4-RB3	GOL#2	Data Register	10 0100 1	
2	4	CM-Pulser	4	RM3-RB1	4	RM2-RB1	4	RM3-RB1	3-CCA, 2-GOL		Same as above with different core selected	
2	5	CM-Readout	5	RM3-RB2	5	RM2-RB2	5	RM3-RB2	3-CCA, 2-GOL		Same as above with different core selected	
2	6	RB1	6	RM3-RB3	6	RM2-RB3	6	RM3-RB3	3-CCA, 2-GOL		Same as above with different core selected	
3	7	RB2	7	CM-Pulser	unused		7	CM-Pulser	3-CCA, 2-GOL		Same as above with different core selected	
3	8	RB3	8	CM-Readout	unused		8	CM-Readout	3-CCA, 2-GOL		Same as above with different core selected	
3	9	RB4	unused		unused		unused		3-CCA, 2-GOL		Same as above with different core selected	
4	14	RB5	13	RM2-RB1	11	CM-Pulser	13	RM2-RB1	3-CCA, 2-GOL		Same as above with different core selected	
4	15	RB6	14	RM2-RB2	12	CM-Readout	14	RM2-RB2	3-CCA, 2-GOL		Same as above with different core selected	
4	unused		15	RM2-RB3	unused		15	RM2-RB3	3-CCA, 2-GOL		Same as above with different core selected	
5	16	RB7	16	RM1-RB1	13	RM3-RB1	16	RM1-RB1	3-CCA, 2-GOL		Same as above with different core selected	
5	17	RB8	17	RM1-RB2	14	RM3-RB2	17	RM1-RB2	3-CCA, 2-GOL		Same as above with different core selected	
5	18	RB9	18	RM1-RB3	15	RM3-RB3	18	RM1-RB3	3-CCA, 2-GOL		Same as above with different core selected	
6	19	RB10	unused		16	RM4-RB1	unused		3-CCA, 2-GOL		Same as above with different core selected	
6	20	RB11	unused		17	RM4-RB2	unused		3-CCA, 2-GOL		Same as above with different core selected	
6	21	RB12	unused		18	RM4-RB3	unused		3-CCA, 2-GOL		Same as above with different core selected	
7	11	CCM-Brd5	10	CCM-Brd5	9	CCM-Brd5	10	CCM-Brd5	TTCrx	Pointer Register	01 1110 0	This address is hardwired on the CCM
7	11	CCM-Brd5	10	CCM-Brd5	9	CCM-Brd5	10	CCM-Brd5	TTCrx	Data Register	01 1110 1	
		I2C Address	**TTCrx Registers**					I2C Address	**CCA Registers**			
		00h	Fine Delay 1					00h	Control			
		01h	Fine Delay 2					01h	Alingment Control Channel a			
		02h	Coarse Delay					02h	Alingment Control Channel b			
		03h	Control					03h	Pedastal DAC			
			See the TTCrx spec for the rest of the registers					04h	DLL Tap Select 0			
								05h	DLL Tap Select 1			
		I2C Address	**GOL Registers**					06h	Test Pulse Bunch Count Match (LSB)			
		00h	Config 0					07h	Test Pulse Bunch Count Match (MSB)			
		01h	Config 1					08h	Test Pattern Byte 0			
		02h	Config 2					09h to 1Bh	Test Pattern Byte 1 to 19			
		03h	Config 3									
		04h	Status 0									
		05h	Status 1									