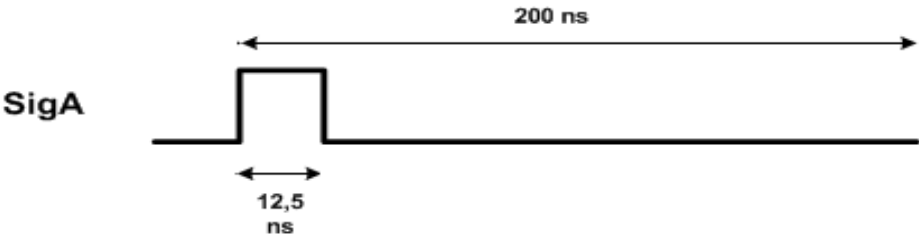
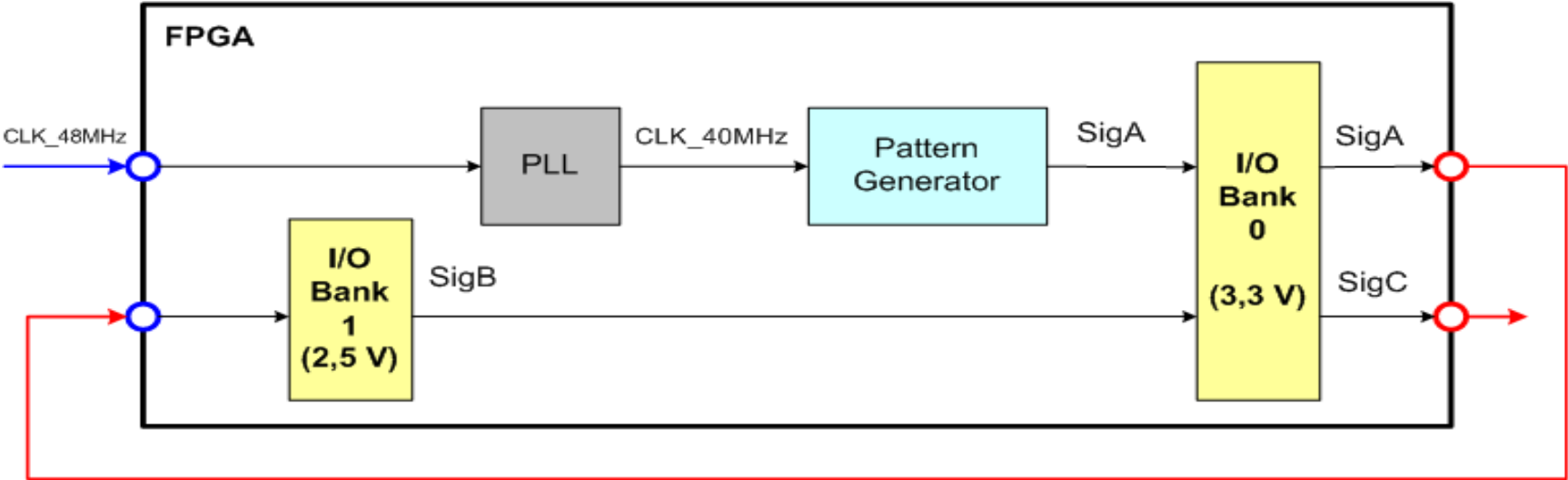


Test Report

- ProASIC Demo Board
- I/O compatibility (3.3 V to 2.5 V)
- Schematic
- Results
 - Open loop measurements
 - Closed loop measurements

Schematic



Results

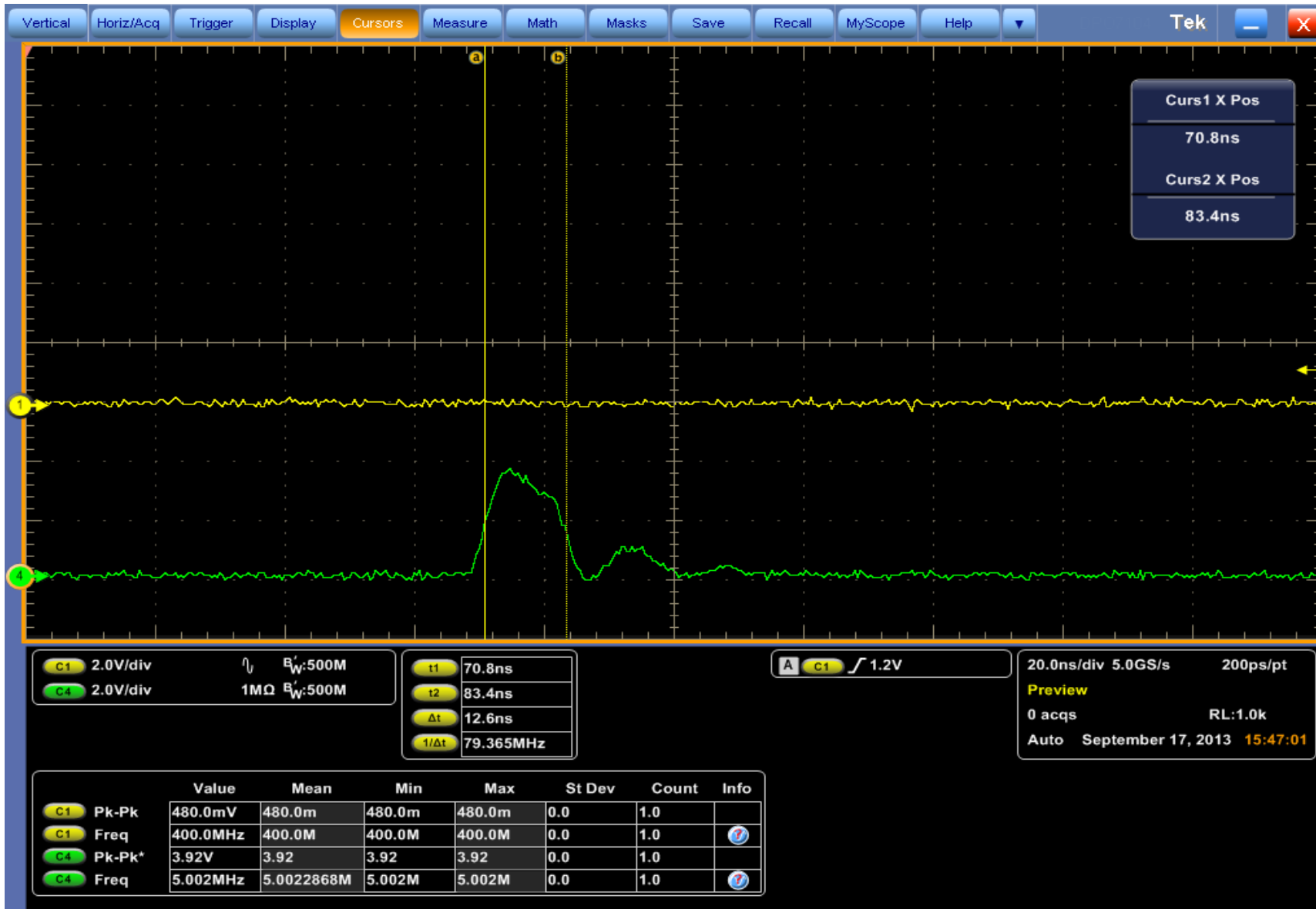
Open Loop Measurements



SigA
(output 3V3)

Results

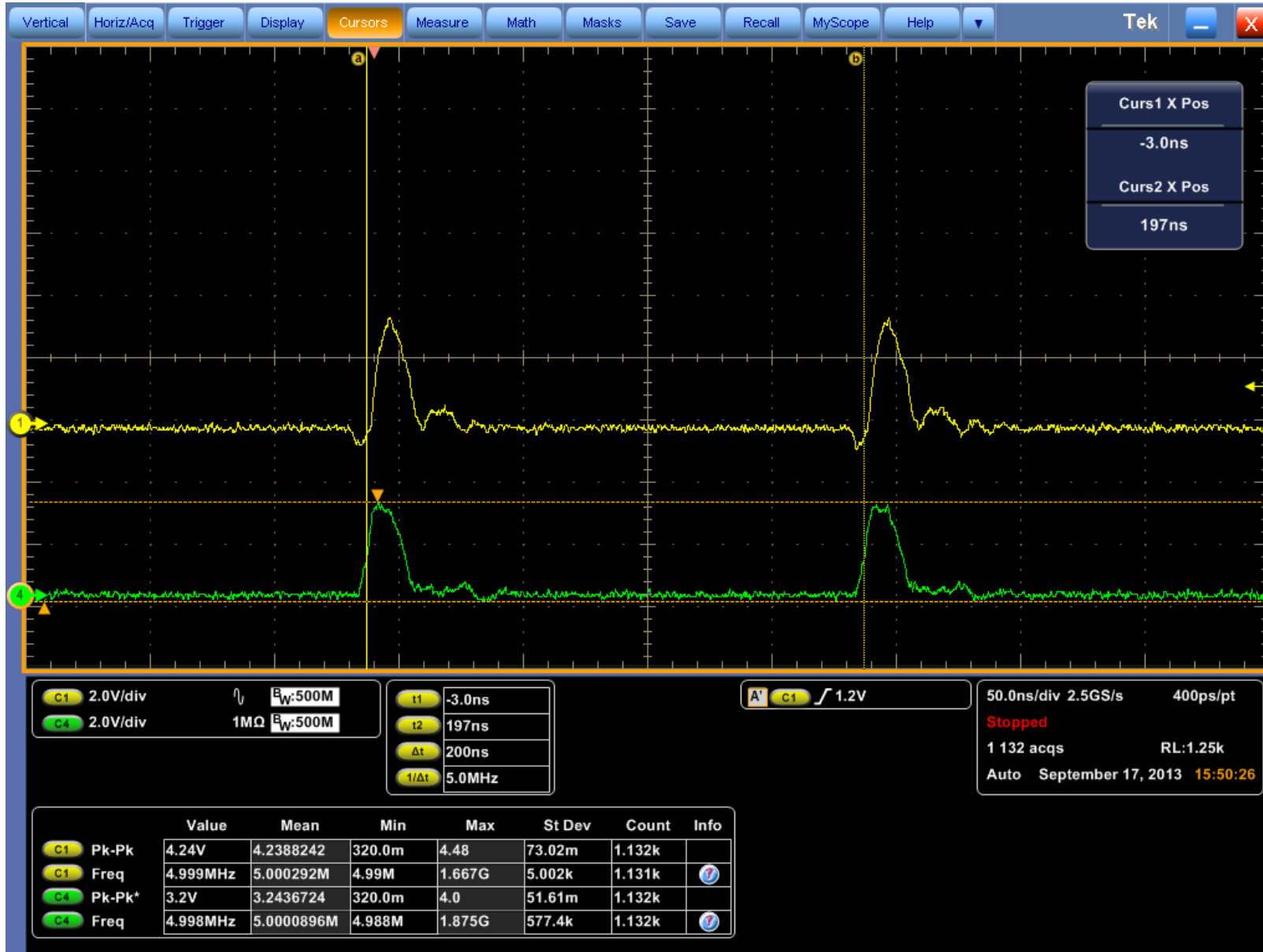
Open Loop Measurements (Zoom)



SigA
(output 3V3)

Results

Closed Loop Measurements

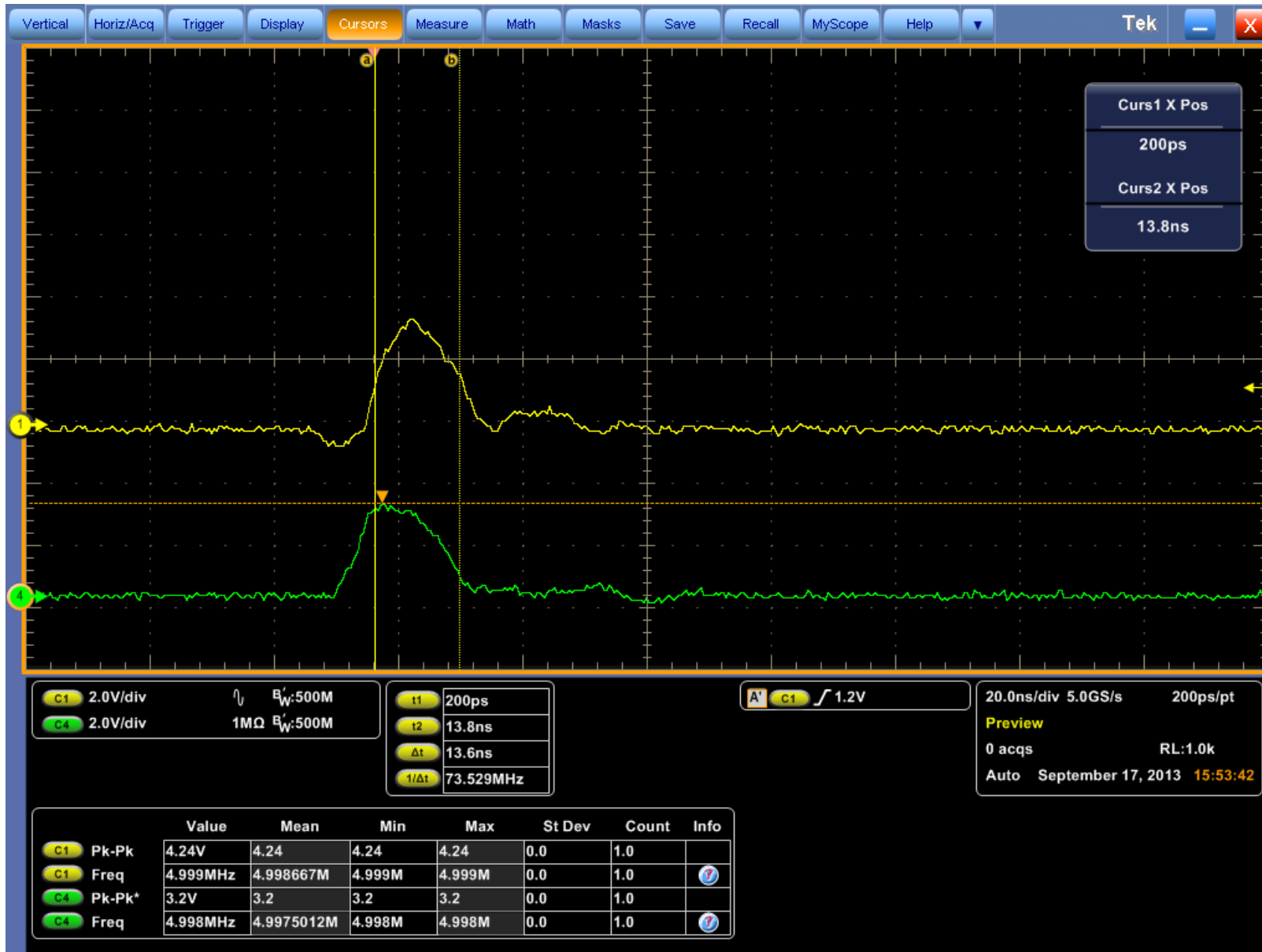


SigC
(output 3V3)

SigA
(output 3V3)

Results

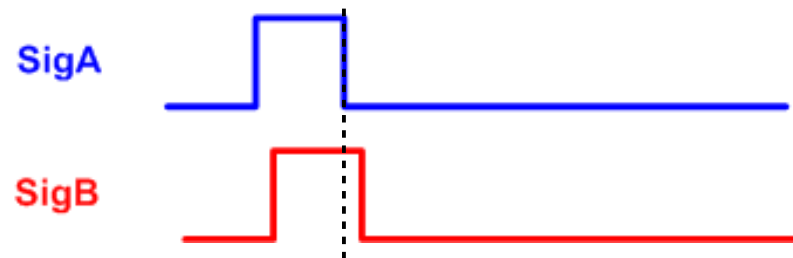
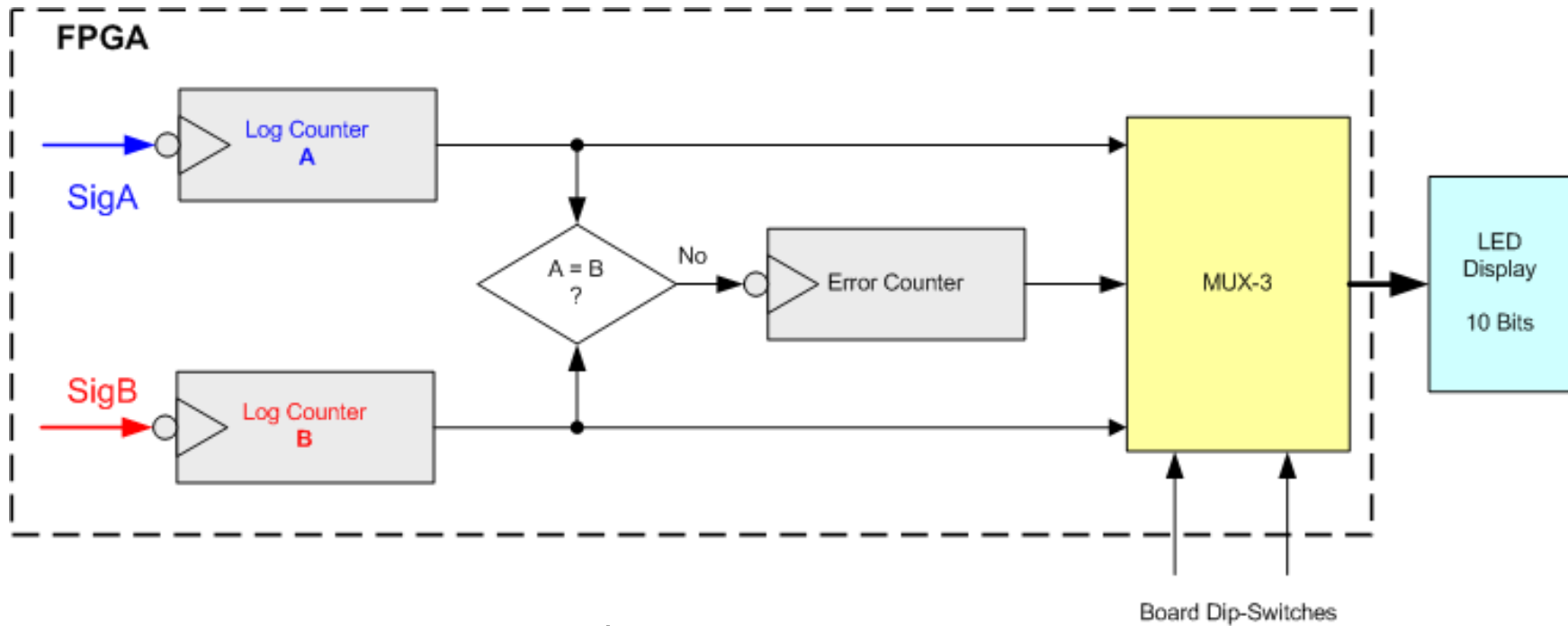
Closed Loop Measurements (zoom)



SigC
(output 3V3)

SigA
(output 3V3)

Bit Error Test



Bit Error Test

- The bit error test was done using two log counters for the events of SigA (3.3V) and SigB (2.5V) and a third counter to compute the difference between the events of SigA and SigB in the falling edge of SigA.
- Using the board switch as output, we could observe the progress of the two counters and the difference between them in the LEDs.
- The simulation spent 15 min, which means $4.5 * 10^9$ events.
- We did not observe any difference, i. e., the difference counter is always equal zero.